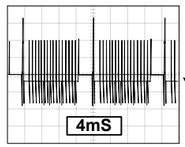
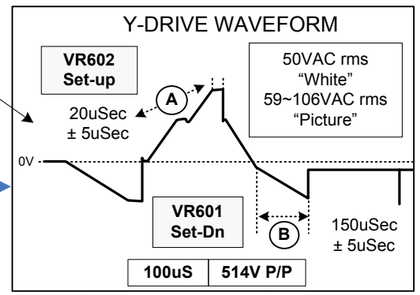
60PS11 (60H3 Panel) CIRCUIT INTERCONNECT DIAGRAM

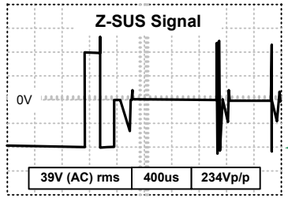
NOTE: Diode tests are conducted with the board disconnected.

Step 1: RL On command turns on 5V (pins 9, 10, 12) and 12V (pins 5,6). Also 5V Det from SMPS when 5V turns on.
Step 2: M5 On command Turns on M5V
Step 3: Vs On command Turns on Va first then VS. Also, 17V turns on with Vs On command



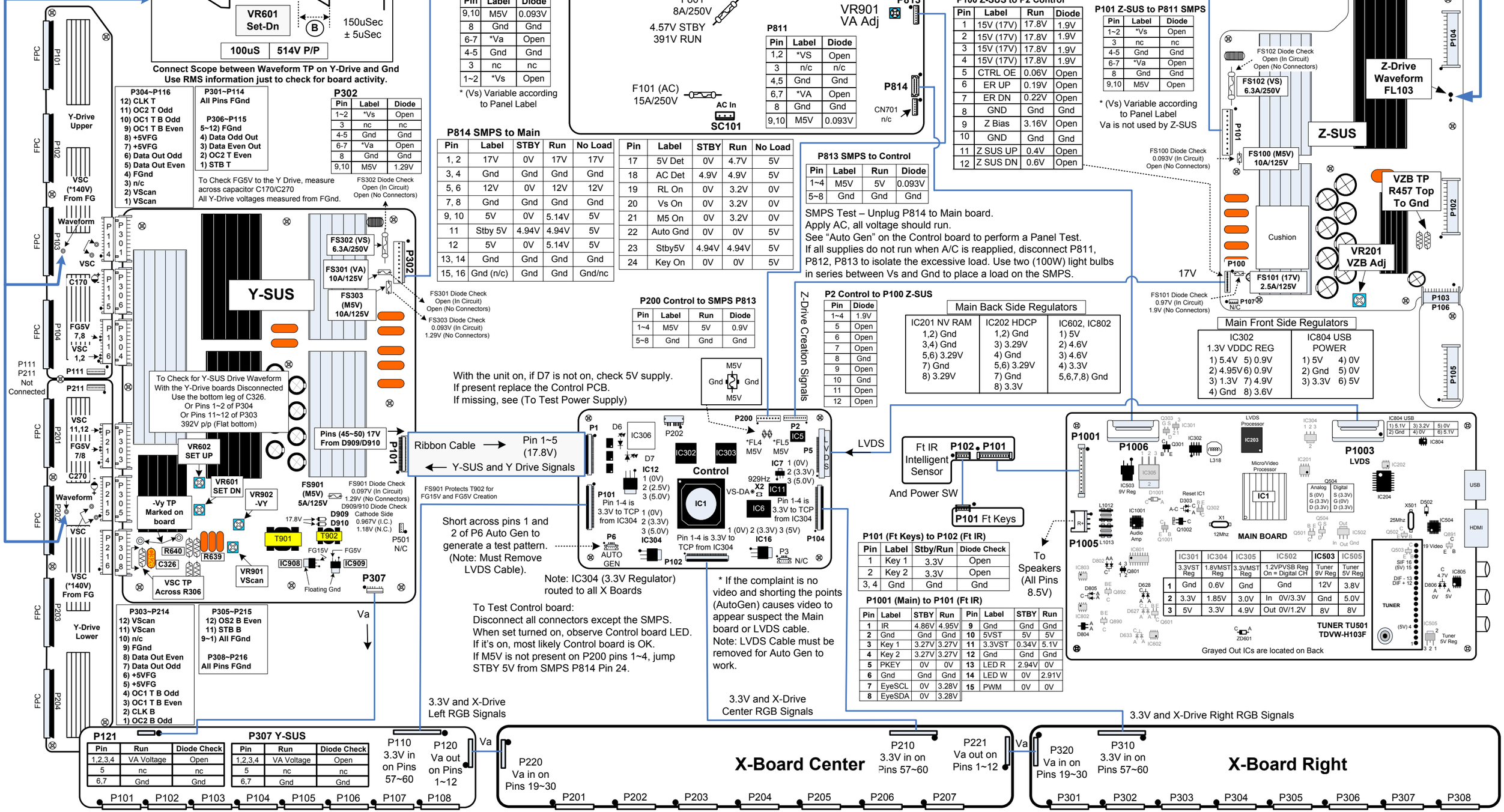


Connect Scope between Waveform TP on Y-Drive and Gnd Use RMS information just to check for board activity.



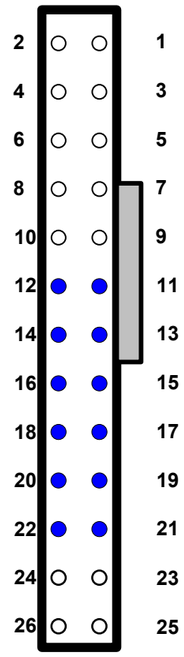
Connect Scope between Waveform TP on Z board and Gnd Use RMS information just to check for board activity.

To run the Z-SUS without the Y-SUS.
Jump Audio 17V from SMPS to pin 1 of P100.
SMPS must be running OK.



60PS11 LVDS P1003 WAVEFORMS

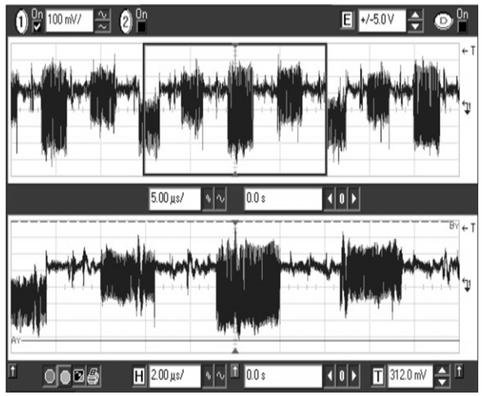
Connector P1003 Configuration



NOTE: LVDS P1003 Information
There are actually 12 pins carrying Video 2 pins are carrying clock signals (17 and 18) to the Control board. With high activity video, pins 21 and 22 would have signals present.

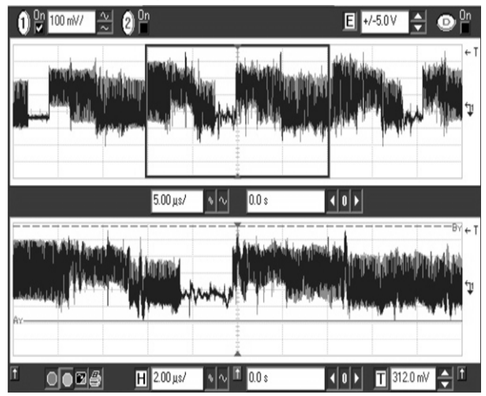
WAVEFORMS:
Waveforms taken using SMTP Color Bar input. All readings give their Time Base related to scope settings.
All waveforms taken from the P1003.

P1003 LVDS (Pin 11) 5uSec / 718mV



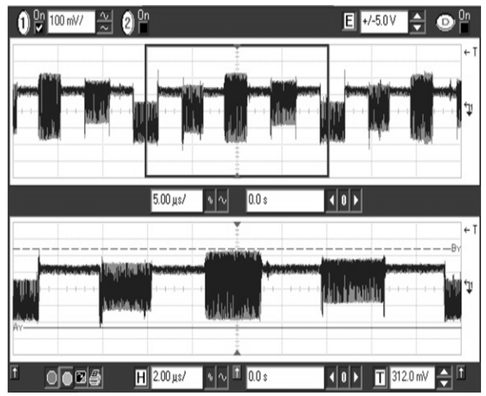
P1003 LVDS (Pin 11) 2uSec / 718mV

P1003 LVDS (Pin 12) 5uSec / 565mV



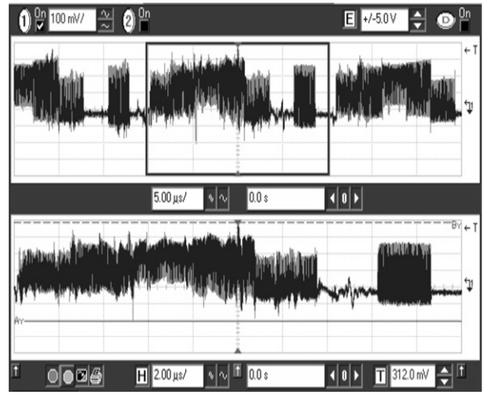
P1003 LVDS (Pin 12) 2uSec / 565mV

P1003 LVDS (Pin 13) 5uSec / 479mV



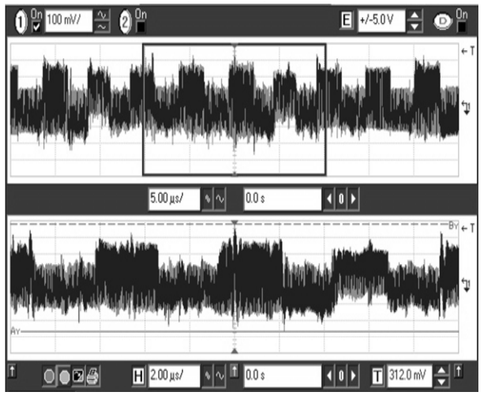
P1003 LVDS (Pin 13) 2uSec / 479mV

P1003 LVDS (Pin 14) 5uSec / 594mV



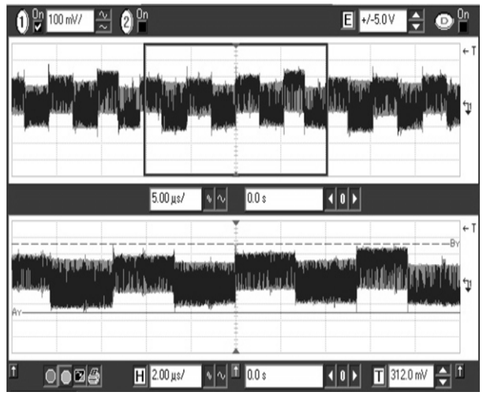
P1003 LVDS (Pin 14) 2uSec / 594mV

P1003 LVDS (Pin 15) 5uSec / 648mV



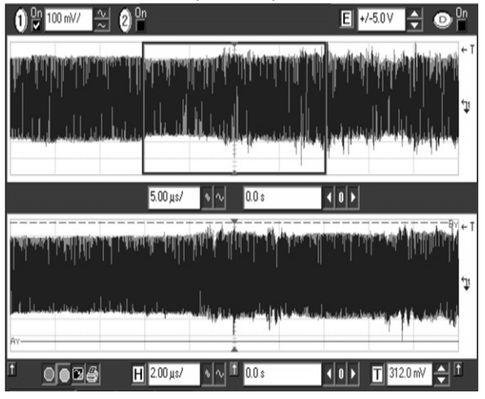
P1003 LVDS (Pin 15) 2uSec / 648mV

P1003 LVDS (Pin 16) 5uSec / 414mV



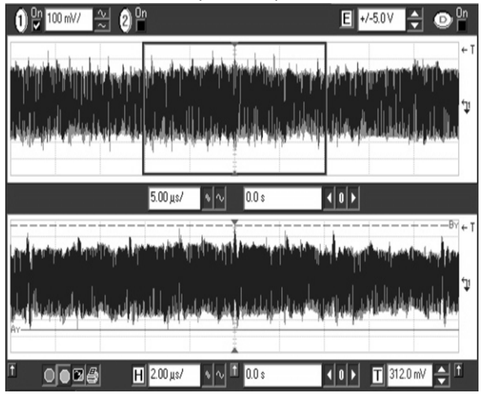
P1003 LVDS (Pin 16) 2uSec / 414mV

P1003 LVDS (Pin 17) 5uSec / 717mV



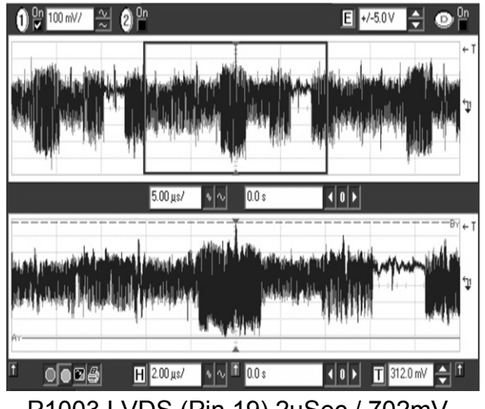
P1003 LVDS (Pin 17) 2uSec / 717mV

P1003 LVDS (Pin 18) 5uSec / 624mV



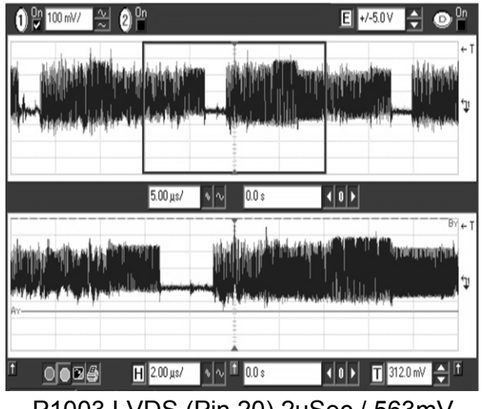
P1003 LVDS (Pin 18) 2uSec / 624mV

P1003 LVDS (Pin 19) 5uSec / 702mV



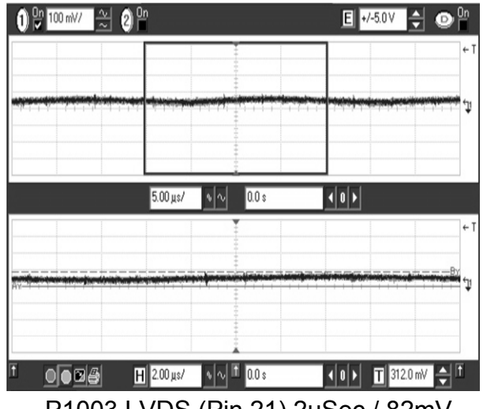
P1003 LVDS (Pin 19) 2uSec / 702mV

P1003 LVDS (Pin 20) 5uSec / 563mV



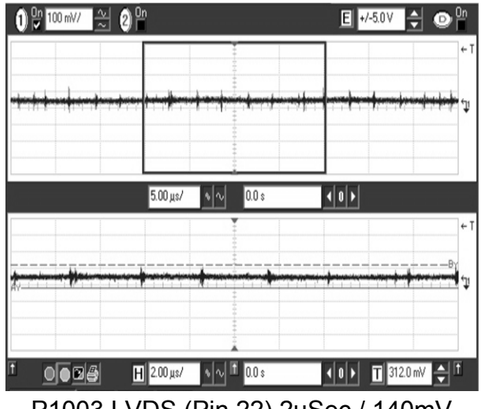
P1003 LVDS (Pin 20) 2uSec / 563mV

P1003 LVDS (Pin 21) 5uSec / 82mV



P1003 LVDS (Pin 21) 2uSec / 82mV

P1003 LVDS (Pin 22) 5uSec / 140mV



P1003 LVDS (Pin 22) 2uSec / 140mV